

Amendments to the Specification

Please amend paragraphs [0045] and [0046] as follows.

[0045] As mentioned above, the present invention may be used for any number of signals. Although only two signals have been thus described with regard to the present invention, FIGURE [[5]] 3 shows an example of the underlying inventive concepts applied to a three-sector arrangement.

[0046] FIGURE [[5]] 3 shows a block diagram processing three (3) input signals 40, 41, 42 in accordance with the present invention. Each input signal 40, 41, 42 is modulated via modulators 43, 44, 45 as described above with regard to FIGURE 1. The modulated signals are then combined by combiner 46 and passed through a length of RF cabling 47. Though not shown, multiple HPAs or a single HPA may be provided in a manner as described above during forward link transmission. After transmission through the RF cabling 46, the aggregate signal stream passes through first splitter 50 and passes through a first and second demux (collectively 50-56 and 60-66, respectively) that are arranged in a cascaded manner. TABLE 2 below shows the signal processing up to the length of RF cabling 47.